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APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
09/981,511	10/17/2001	Raymond J. Iannuzzelli	1662-46500 JMH (P01-3668)	6896

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EXAMINER

LEON, EDWIN A

ART UNIT	PAPER NUMBER
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2833

DATE MAILED: 06/05/2002

Please find below and/or attached an Office communication concerning this application or proceeding.

Office Action Summary

Applicati n No.

09/981,511

Applicant(s)

IANNUZZELLI ET AL.

Examiner

Edwin A. León

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-- The MAILING DATE of this communication appears on the cover sheet with the correspondence address --

Period for Reply

A SHORTENED STATUTORY PERIOD FOR REPLY IS SET TO EXPIRE 3 MONTH(S) FROM THE MAILING DATE OF THIS COMMUNICATION.

- Extensions of time may be available under the provisions of 37 CFR 1.136(a). In no event, however, may a reply be timely filed after SIX (6) MONTHS from the mailing date of this communication.
- If the period for reply specified above is less than thirty (30) days, a reply within the statutory minimum of thirty (30) days will be considered timely.
- If NO period for reply is specified above, the maximum statutory period will apply and will expire SIX (6) MONTHS from the mailing date of this communication.
- Failure to reply within the set or extended period for reply will, by statute, cause the application to become ABANDONED (35 U.S.C. § 133).
- Any reply received by the Office later than three months after the mailing date of this communication, even if timely filed, may reduce any earned patent term adjustment. See 37 CFR 1.704(b).

Status

- 1) ☐ Responsive to communication(s) filed on ____.
- 2a) ☐ This action is **FINAL**. 2b) ☒ This action is non-final.
- 3) ☐ Since this application is in condition for allowance except for formal matters, prosecution as to the merits is closed in accordance with the practice under *Ex parte Quayle*, 1935 C.D. 11, 453 O.G. 213.

Disposition of Claims

- 4) ☒ Claim(s) 1-20 is/are pending in the application.
- 4a) Of the above claim(s) ____ is/are withdrawn from consideration.
- 5) ☐ Claim(s) ____ is/are allowed.
- 6) ☒ Claim(s) 1-20 is/are rejected.
- 7) ☐ Claim(s) ____ is/are objected to.
- 8) ☐ Claim(s) ____ are subject to restriction and/or election requirement.

Application Papers

- 9) ☐ The specification is objected to by the Examiner.
- 10) ☐ The drawing(s) filed on ____ is/are: a) ☐ accepted or b) ☐ objected to by the Examiner.
- Applicant may not request that any objection to the drawing(s) be held in abeyance. See 37 CFR 1.85(a).
- 11) ☐ The proposed drawing correction filed on ____ is: a) ☐ approved b) ☐ disapproved by the Examiner.
- If approved, corrected drawings are required in reply to this Office action.
- 12) ☐ The oath or declaration is objected to by the Examiner.

Priority under 35 U.S.C. §§ 119 and 120

- 13) ☐ Acknowledgment is made of a claim for foreign priority under 35 U.S.C. § 119(a)-(d) or (f).
- a) ☐ All b) ☐ Some * c) ☐ None of:
1. ☐ Certified copies of the priority documents have been received.
 2. ☐ Certified copies of the priority documents have been received in Application No. ____.
 3. ☐ Copies of the certified copies of the priority documents have been received in this National Stage application from the International Bureau (PCT Rule 17.2(a)).
- * See the attached detailed Office action for a list of the certified copies not received.
- 14) ☐ Acknowledgment is made of a claim for domestic priority under 35 U.S.C. § 119(e) (to a provisional application).
- a) ☐ The translation of the foreign language provisional application has been received.
- 15) ☐ Acknowledgment is made of a claim for domestic priority under 35 U.S.C. §§ 120 and/or 121.

Attachment(s)

- 1) ☒ Notice of References Cited (PTO-892) 4) ☐ Interview Summary (PTO-413) Paper No(s). ____
- 2) ☐ Notice of Draftsperson's Patent Drawing Review (PTO-948) 5) ☐ Notice of Informal Patent Application (PTO-152)
- 3) ☐ Information Disclosure Statement(s) (PTO-1449) Paper No(s) ____ 6) ☐ Other: ____

DETAILED ACTION

Claim Rejections - 35 USC § 103

1. The following is a quotation of 35 U.S.C. 103(a) which forms the basis for all obviousness rejections set forth in this Office action:

(a) A patent may not be obtained though the invention is not identically disclosed or described as set forth in section 102 of this title, if the differences between the subject matter sought to be patented and the prior art are such that the subject matter as a whole would have been obvious at the time the invention was made to a person having ordinary skill in the art to which said subject matter pertains. Patentability shall not be negated by the manner in which the invention was made.

2. Claims 1-20 are rejected under 35 U.S.C. 103(a) as being unpatentable over Applicant's admitted prior art in view of Mori (U.S. Patent No. 5,318,456). With regard to Claim 1, Applicant's admitted prior art discloses a component restraint computer system that is used to secure an electronic component (10) to a circuit board (22), comprising a backing plate (14); a spring (18); a post (20) extending from the backing plate (14), the post (20) having a stop surface (16). See Fig. 1 and Pages 1-3.

Applicant's admitted prior art doesn't show a clip inserted onto the post, and pushed down the post towards the backing plate until the clip engages the stop surface of the post, the clip compressing the spring as the clip is pushed toward the stop surface.

Mori discloses a system using a clip (5a,5b) inserted onto a post (55), being pushed down the post (55) towards a backing plate (24c) until the clip (5a,5b) engages a stop surface (24a,24b) of the post (55). See Fig. 1.

Thus, it would have been obvious to one with ordinary skill in the art to modify the system of Applicant's admitted prior art by including a clip inserted onto a post, being

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pushed down the post towards a backing plate until the clip engages a stop surface of the post as taught in Morin to improve the contact pressure between the parts.

With regard to Claims 2 and 4, Morin discloses the post (55) having an upper end distal from the backing plate (24c) and the clip (5a,5b) for each post (55) is pushed down over the upper end until the clip (5a,5b) engage the stop surface (24a, 24b) of the posts (55). See Fig. 1.

With regard to Claims 3 and 10, Applicant's admitted prior art discloses four posts (20) protruding from the backing plate (14) and including stop surfaces (16), each post (20) having a spring (18) disposed thereon. See Fig. 1 and Pages 1-3.

With regard to Claim 5, Applicant's admitted prior art discloses the electronic component (10) and circuit board (22) being disposed between the backing plate (14) and the springs (18) and the electronic component (10) is secured to the circuit board (22). See Fig. 1 and Pages 1-3.

With regard to Claim 6, Applicant's admitted prior art discloses the heat sink (12) also disposed between the backing plate (14) and the springs (18), the heat sink (12) further disposed between the electronic component (10) and the springs (18). See Fig. 1 and Pages 1-3.

With regard to Claim 7, Mori discloses the upper ends of the post (55) comprise tips formed between the distal end of the post (55) and the stop surface (24a,24b), each tip having a smaller cross section at its distal end than at the stop surface (24a,24b). See Fig. 1.

With regard to Claim 8, Mori discloses the upper end of the post (55) being substantially conically shaped. See Fig. 1.

With regard to Claim 9, Mori discloses the clip (5a,5b) including protruding members, which define a hole in which the post (55) is inserted, the protruding members are pushed apart as the clip (5a,5b) is pushed along the post (55) towards the stop surface (24a,24b). See Fig. 1.

With regard to Claim 11, Applicant's admitted prior art discloses a component restraint computer system that is used to secure an electronic component (10) to a circuit board (22), and having a processor and heat sink (12); an output device coupled to the processor comprising: a backing plate (14); a plurality of springs (18); a plurality of posts (20) extending from the backing (flat) plate or surface (14) through the circuit board (22) and the springs (18), each post (20) having a stop surface (16); the electronic component (10) and heat sink (12) sandwiched between the circuit board (22) and the springs (18). See Fig. 1 and Pages 1-3.

Applicant's admitted prior art doesn't show a plurality of clips, one clip per post, which, when inserted onto the posts, are pushed down the posts towards the backing plate until the clips engage the stop surfaces of the posts, the clips compress the springs as the clips are pushed toward the stop surfaces.

Mori discloses a system using a clip (5a,5b) inserted onto a post (55), being pushed down the post (55) towards a backing plate (24c) until the clip (5a,5b) engages a stop surface (24a,24b) of the post (55). See Fig. 1.

Thus, it would have been obvious to one with ordinary skill in the art to modify the system of Applicant's admitted prior art by including a clip inserted onto a post, being pushed down the post towards a backing plate until the clip engages a stop surface of the post as taught in Morin to improve the contact pressure between the parts.

With regard to Claims 12 and 14, Mori discloses each clip (5a,5b) including protruding members, which define a hole in which the post (55) is inserted, the protruding members are pushed apart as the clip (5a,5b) is pushed down the post (55) towards the stop surface (24a,24b). See Fig. 1.

With regard to Claim 13, Mori discloses the post (55) having a distal end opposite the backing plate (24c) that includes a tip that has a cross section that increases from the distal end of the post (55) towards the stop surface (24a,24b). See Fig. 1.

With regard to Claim 15, Applicant's admitted prior art discloses a component restraint computer system that is used to secure an electronic component (10) to a circuit board (22), comprising: a flat plate (14); four clip retainer members (18); four posts (20) extending from the flat plate (14) through the circuit board (22) and the clip retainer members (18), each post (20) having a stop surface (16). See Fig. 1 and Pages 1-3.

Applicant's admitted prior art doesn't show a plurality of clips, one clip per post, which, when inserted onto the posts, are pushed down the posts towards the backing plate until the clips engage the stop surfaces of the posts, the clips compress the springs as the clips are pushed toward the stop surfaces.

Mori discloses a system using a clip (5a,5b) inserted onto a post (55), being pushed down the post (55) towards a backing plate (24c) until the clip (5a,5b) engages a stop surface (24a,24b) of the post (55). See Fig. 1.

Thus, it would have been obvious to one with ordinary skill in the art to modify the system of Applicant's admitted prior art by including a clip inserted onto a post, being

pushed down the post towards a backing plate until the clip engages a stop surface of the post as taught in Morin to improve the contact pressure between the parts.

With regard to Claim 16, Applicant's admitted prior art discloses a component restraint computer system that is used to secure a device (10) to a circuit board (22), comprising: a backing surface (14); a plurality of posts (20) extending from the backing surface (14) through the circuit board (22), each post (20) having a stop surface (16). See Fig. 1 and Pages 1-3.

Applicant's admitted prior art doesn't show a plurality of clips, the clips engaging the stop surfaces of the posts.

Mori discloses a system using a clip (5a,5b) inserted onto a post (55), being pushed down the post (55) towards a backing surface (24c) until the clip (5a,5b) engages a stop surface (24a,24b) of the post (55). See Fig. 1.

Thus, it would have been obvious to one with ordinary skill in the art to modify the system of Applicant's admitted prior art by including a clip inserted onto a post, being pushed down the post towards a backing surface until the clip engages a stop surface of the post as taught in Morin to improve the contact pressure between the parts.

With regard to Claim 17, Mori discloses the post (55) having a tip at its distal end that has a cross sectional area that increases from the distal end of the post (55) towards the stop surface (24a,24b). See Fig. 1.

With regard to Claim 18, Mori discloses the tips being substantially conical in shape. See Fig. 1.

With regard to Claims 19-20, Applicant's admitted prior art discloses a component restraint computer system that is used to secure an electronic component

(10) to a circuit board (22), and having a processor and heat sink (12); an output device coupled to the processor comprising: a backing plate (14); a plurality of springs (18); a plurality of posts (20) extending from the backing plate (14) through the circuit board (22) and the springs (18), each post (20) having a stop surface (16); the electronic component (10) and heat sink (12) sandwiched between the circuit board (22) and the springs (18). See Fig. 1 and Pages 1-3.

Applicant's admitted prior art doesn't show a plurality of clips, one clip per post, which, when inserted onto the posts, are pushed down the posts towards the backing plate until the clips engage the stop surfaces of the posts, the clips compress the springs as the clips are pushed toward the stop surfaces.

Mori discloses a system using a clip (5a,5b) inserted onto a post (55), being pushed down the post (55) towards a backing plate (24c) until the clip (5a,5b) engages a stop surface (24a,24b) of the post (55). See Fig. 1.

Thus, it would have been obvious to one with ordinary skill in the art to modify the system of Applicant's admitted prior art by including a clip inserted onto a post, being pushed down the post towards a backing plate until the clip engages a stop surface of the post as taught in Morin to improve the contact pressure between the parts. The method limitations are deemed inherent.

Conclusion

3. The prior art made of record and not relied upon is considered pertinent to applicant's disclosure. Corisis et al. (U.S. Patent No. 6,302,719), Matsuoka et al. (U.S.

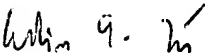
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Patent No. 5,683,262), Ikeya (U.S. Patent No. 5,462,446), Shimada (U.S. Patent No. 6,354,856), Savant (U.S. Patent No. 4,993,955), and Uratsuji (U.S. Patent No. 5,195,903) disclose restraint systems that secure electronic components to circuit boards.

4. Any inquiry concerning this communication or earlier communications from the examiner should be directed to Edwin A. León whose telephone number is (703) 308-6253. The examiner can normally be reached on Monday - Friday 9:00-5:30.

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Paula A. Bradley can be reached on (703) 308-2319. The fax phone numbers for the organization where this application or proceeding is assigned are (703) 308-7722 for regular communications and (703) 308-7722 for After Final communications.

Any inquiry of a general nature or relating to the status of this application or proceeding should be directed to the receptionist whose telephone number is (703) 308-0956.


Edwin A. Leon
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May 28, 2002